FIG.1
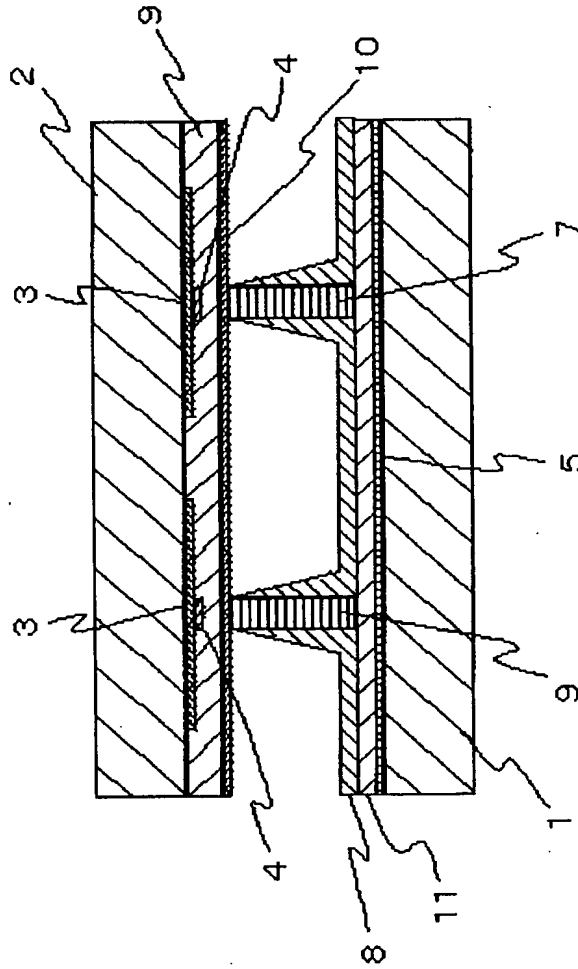## PRIOR ART

FIG.2
PRIOR ART

FIG.3
PRIOR ART

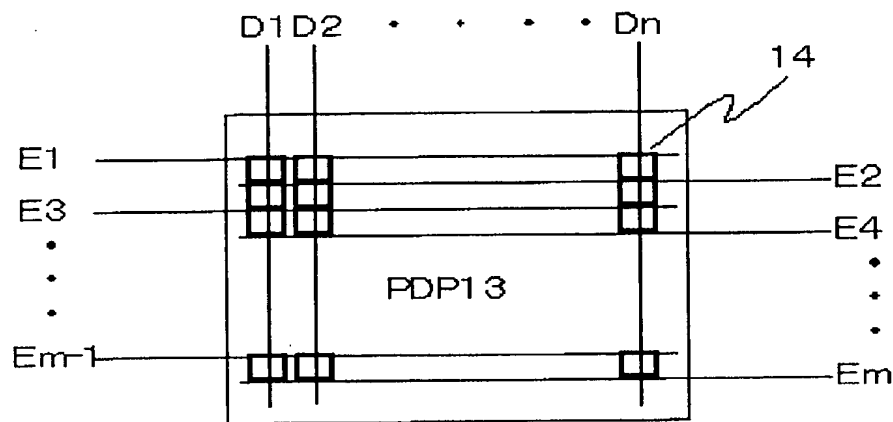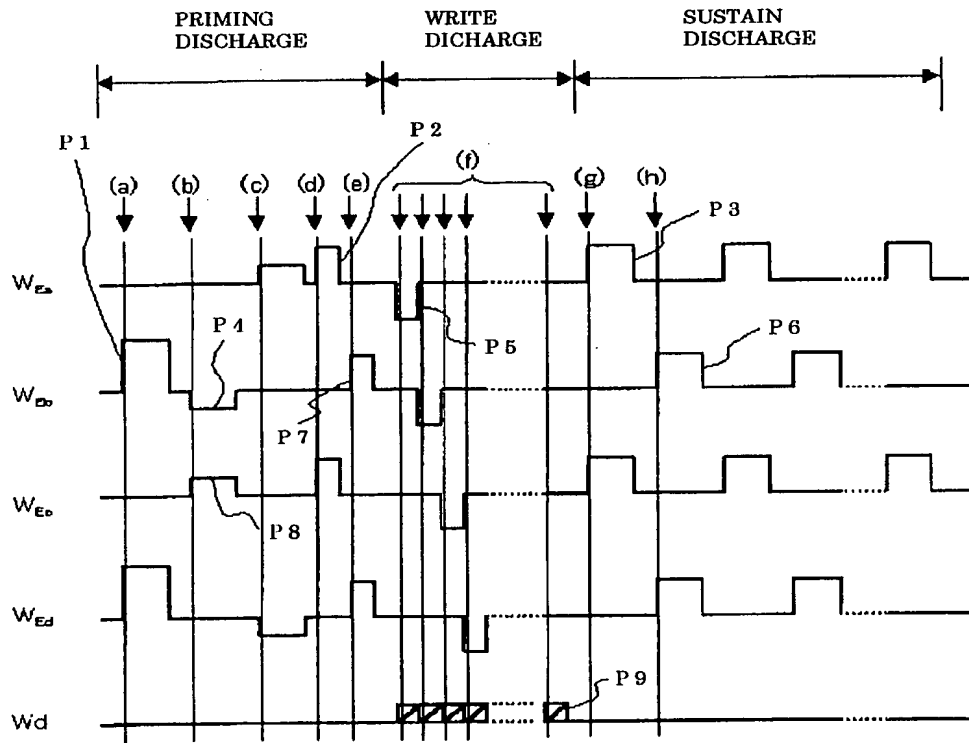# FIG.4
PRIOR ART

P 1 : PRIMING DISCHARGE PULSE Pp1
P 2 : PRIMING DISCHARGE ELIMINATION PULSE Ppe
P 3 : SUSTAIN PULSE
P 4 : PRIMING DISCHARGE PULSE Pp2
P 5 : SCAN PULSE
P 6 : SUSTAIN PULSE
P 7 : PRIMING DISCHARGE ELIMINATION PULSE Ppe
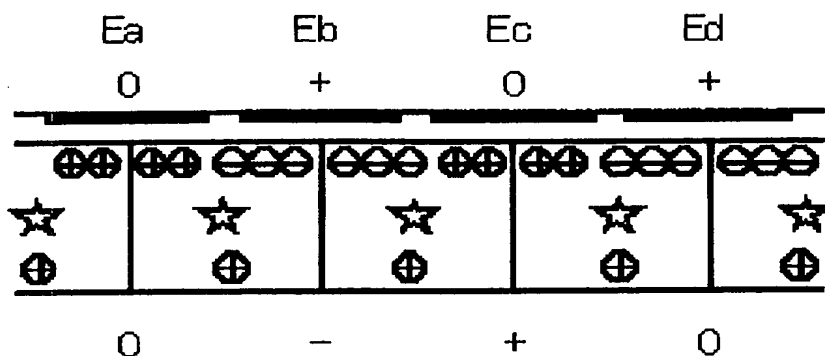P 8 : PRIMING DISCHARGE PULSE Pp3
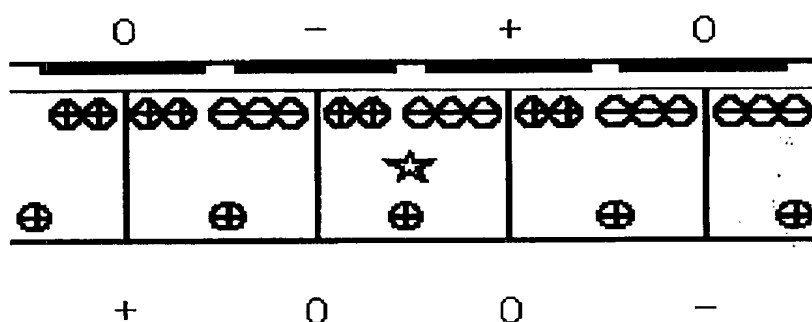P 9 : DATA PULSE

FIG.5A
PRIOR ART

FIG.5B
PRIOR ART

FIG.5C
PRIOR ART

FIG.5D
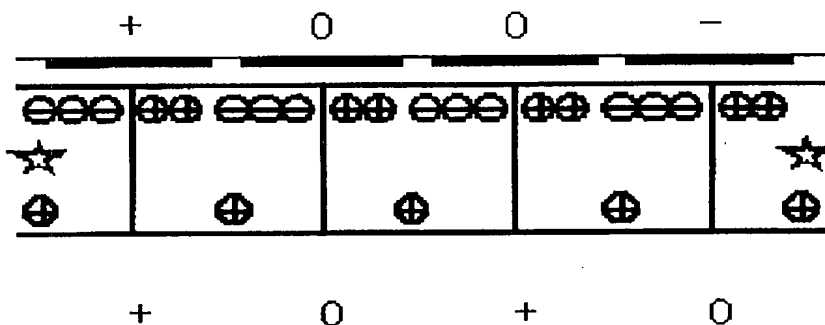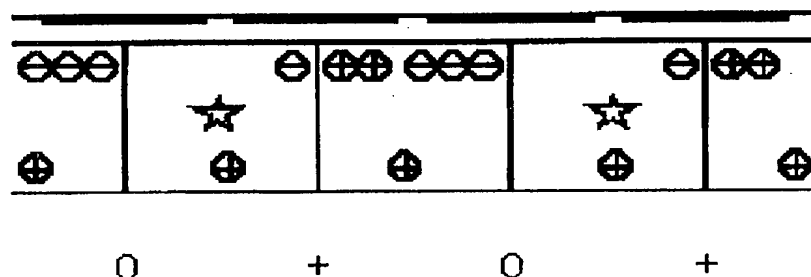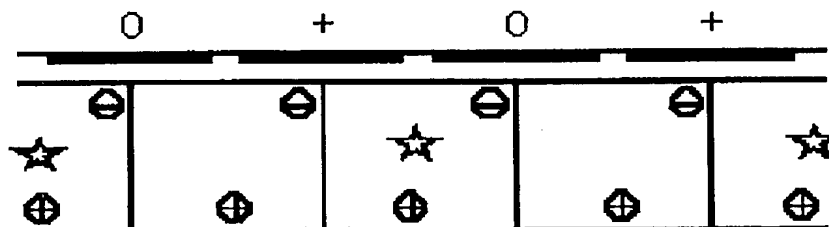PRIOR ART

FIG.5E
PRIOR ART

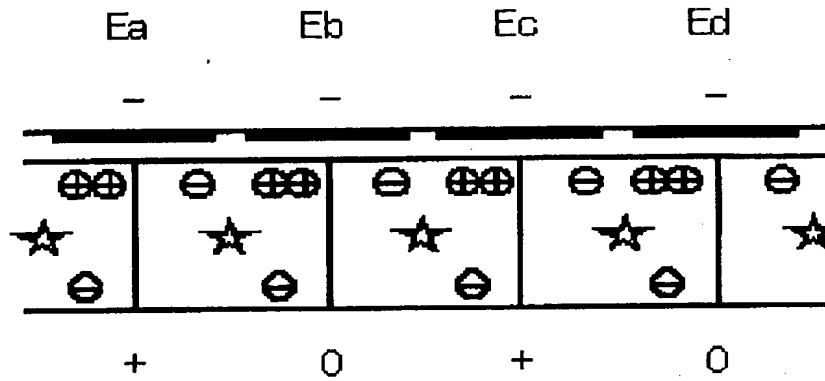Ea   Eb   Ec   Ed

FIG.5F
PRIOR ART

FIG.5G
PRIOR ART

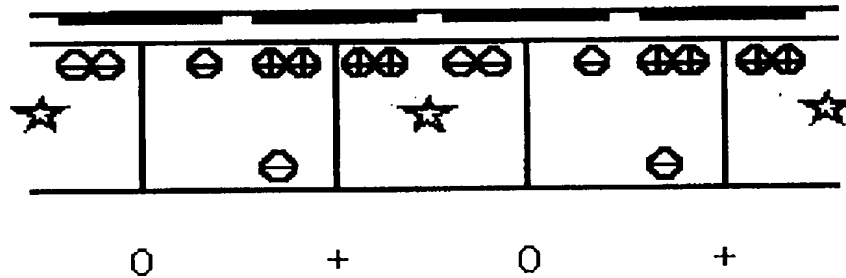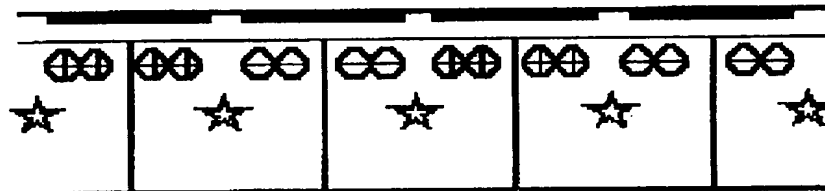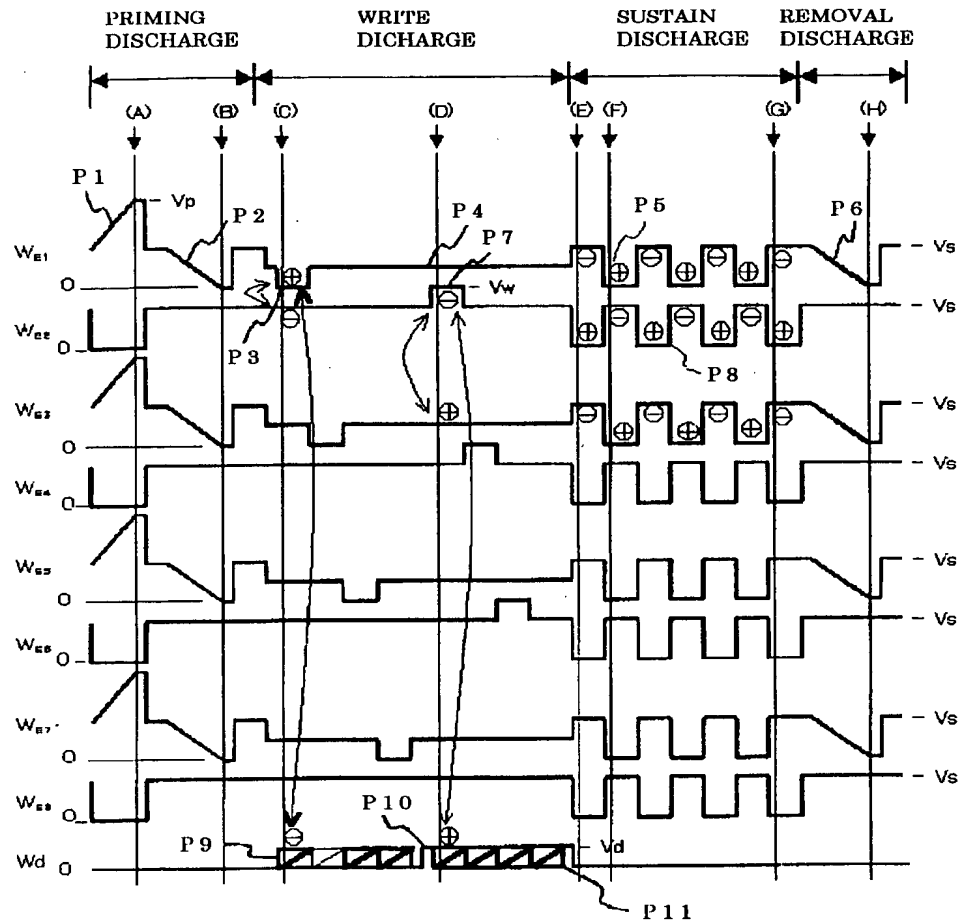**FIG.5H**
PRIOR ART

FIG.6

P1 : PRIMING DISCHARGE PULSE
P2 : PRIMING DISCHARGE REMOVAL PULSE
P3 : SCAN PULSE
P4 : SCAN BASE PULSE
P5 : SUSTAIN PULSE
P6 : SUSTAIN REMOVAL PULSE
P7 : SCAN PULSE
P8 : SUSTAIN PULSE
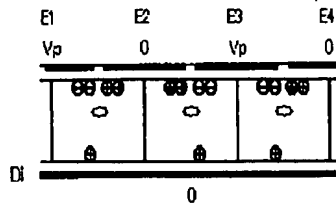P9 : DATA PULSE
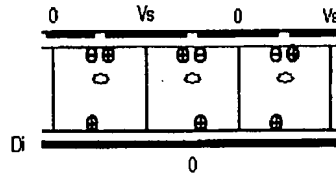P10 : DATA BASE PULSE
P11 : DATA PULSE

FIG.7A

FIG.7B

FIG.7C

FIG.7D

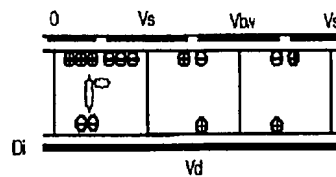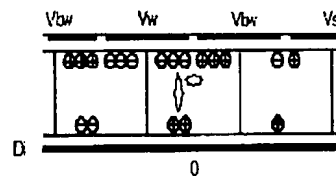FIG.7E

FIG.7F

FIG.7G

FIG.7H

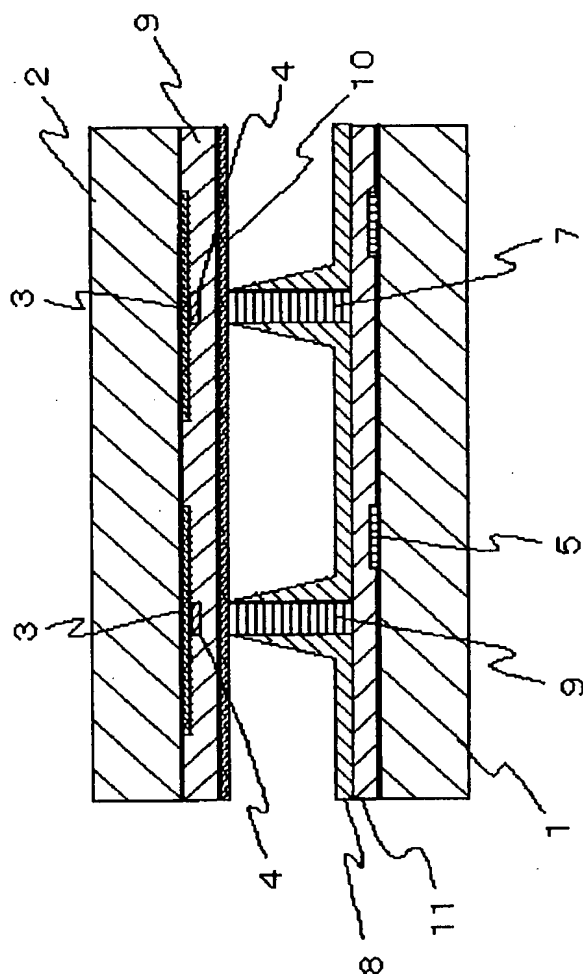FIG.8

FIG.9

FIG.10

FIG.11

FIG.12

U.S. Application of: T. Nakamura
09/891,664, filed June 27, 2001
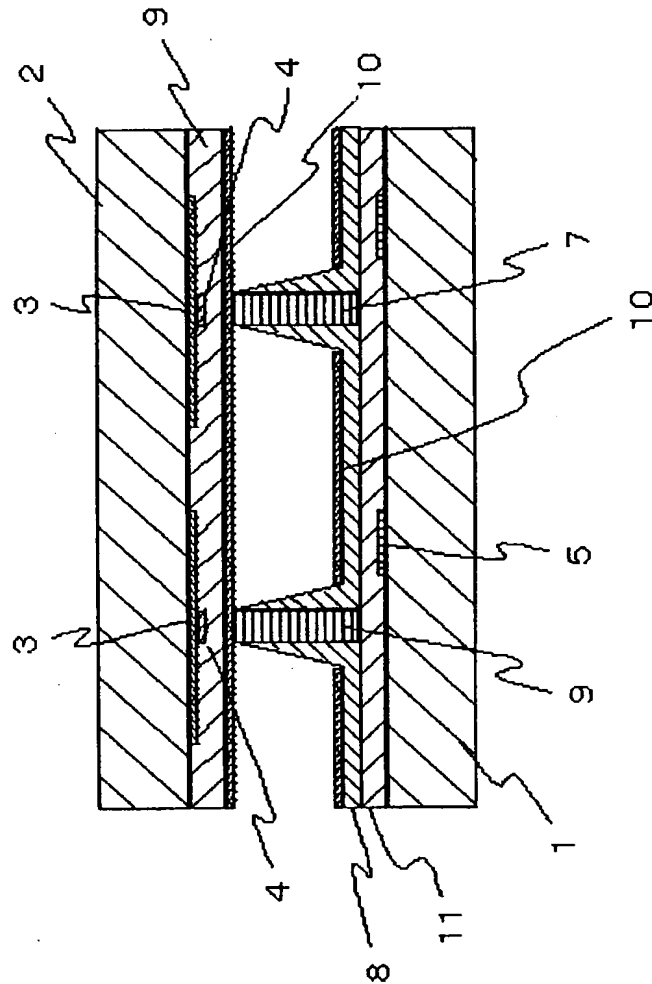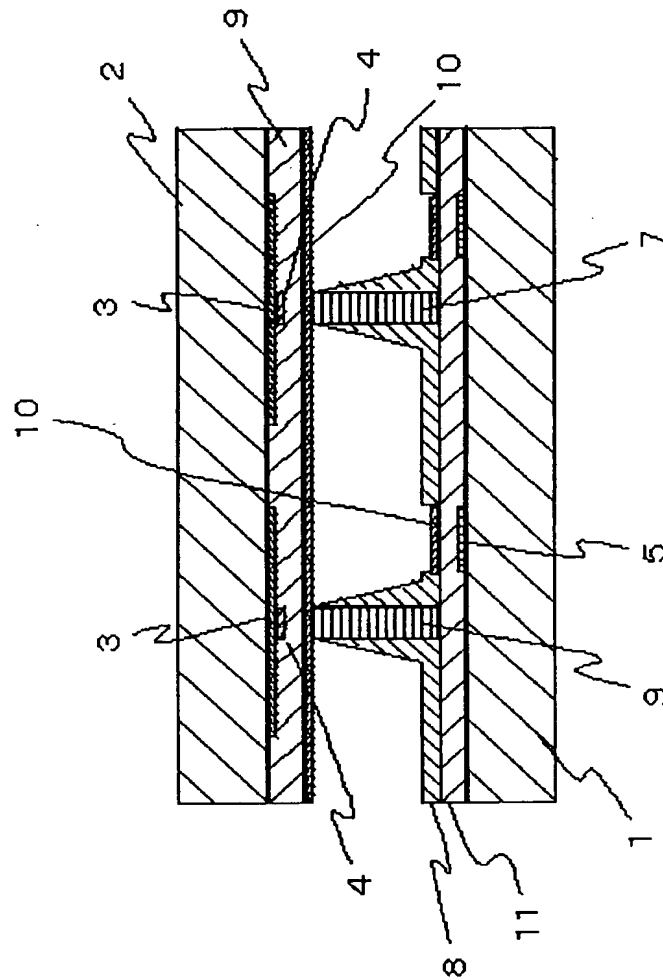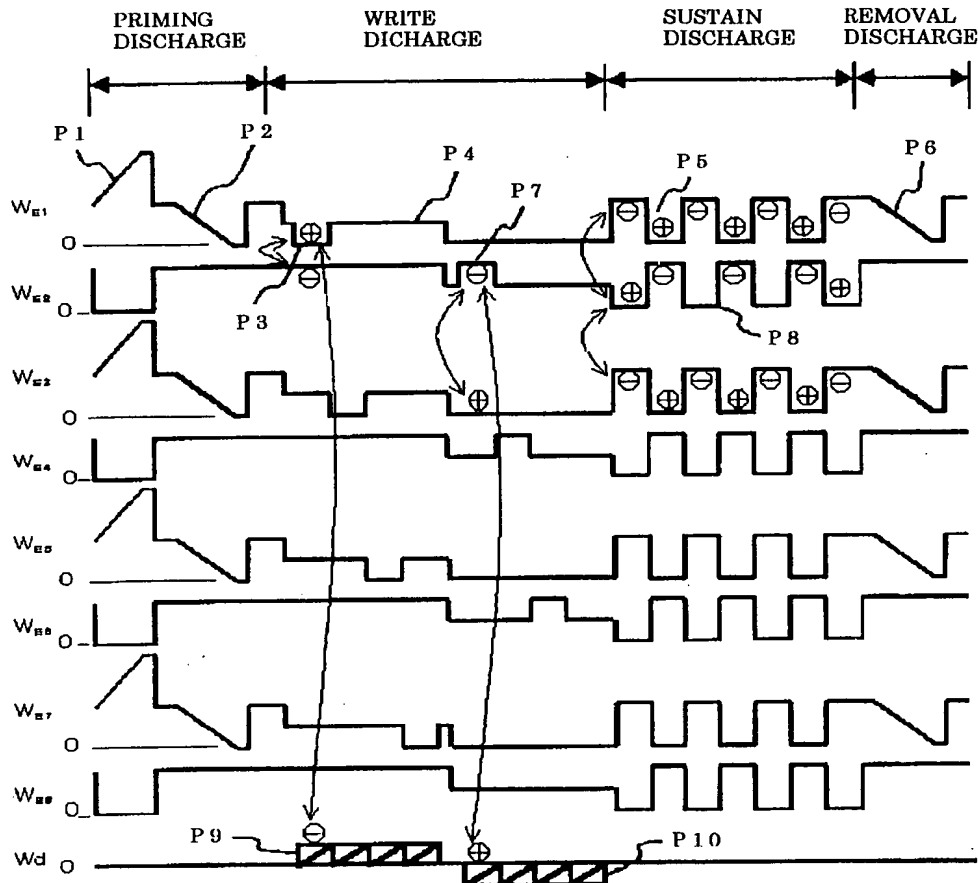METHOD OF DRIVING
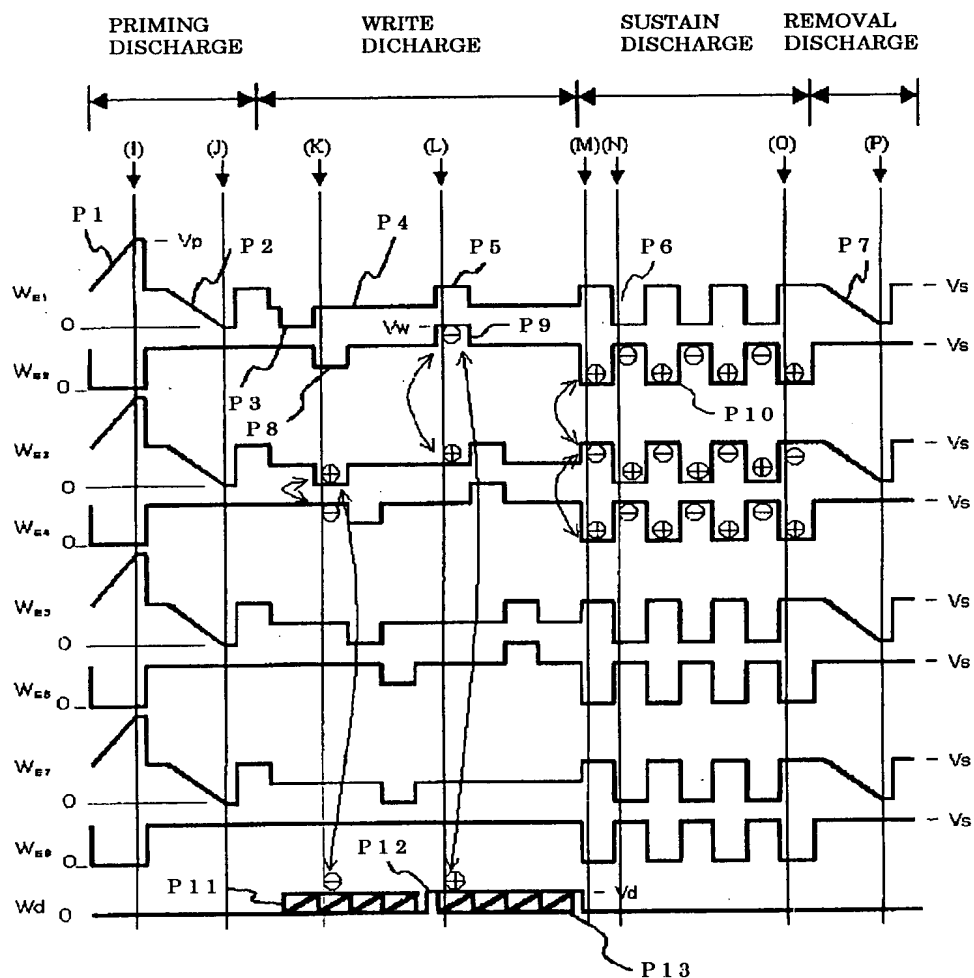PLASMA DISPLAY PANEL
Q65228 - Sheet 14 of 16
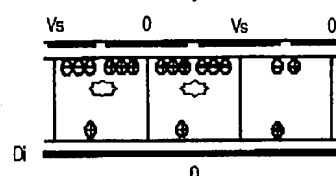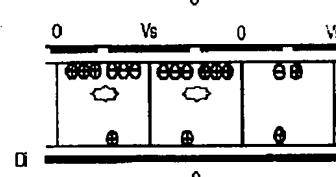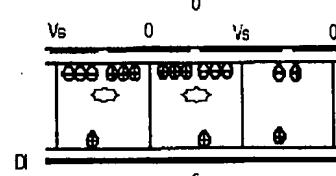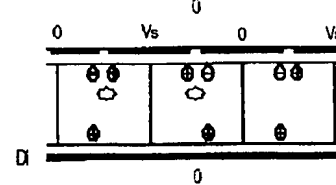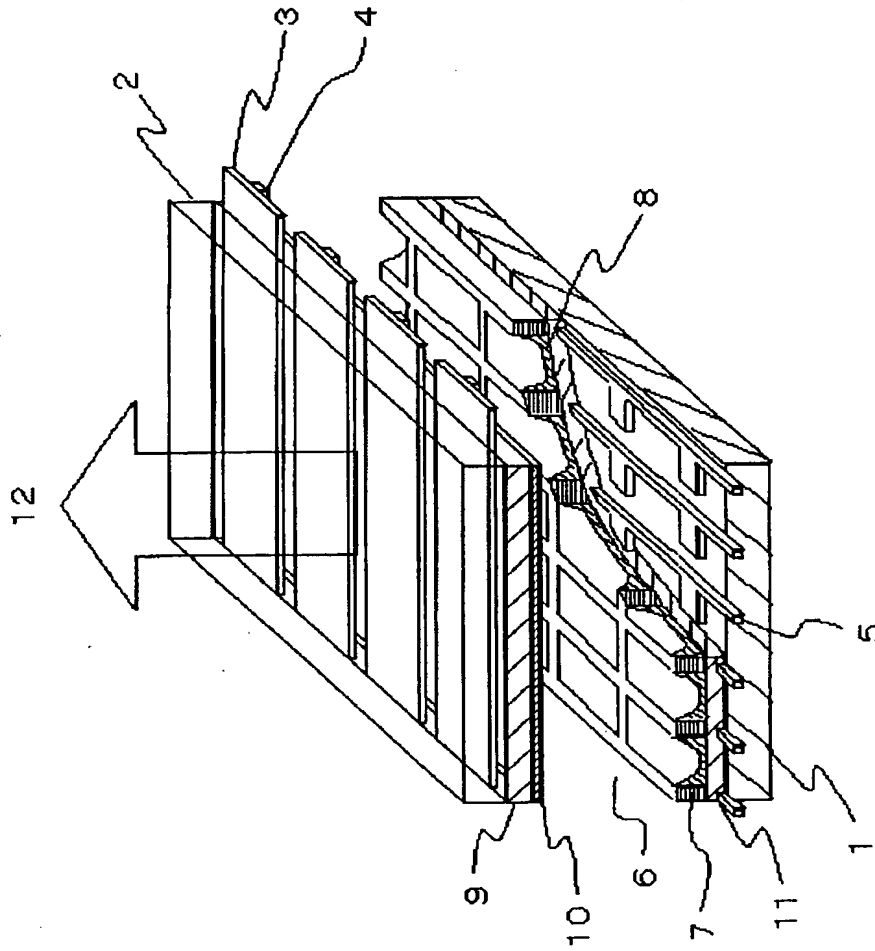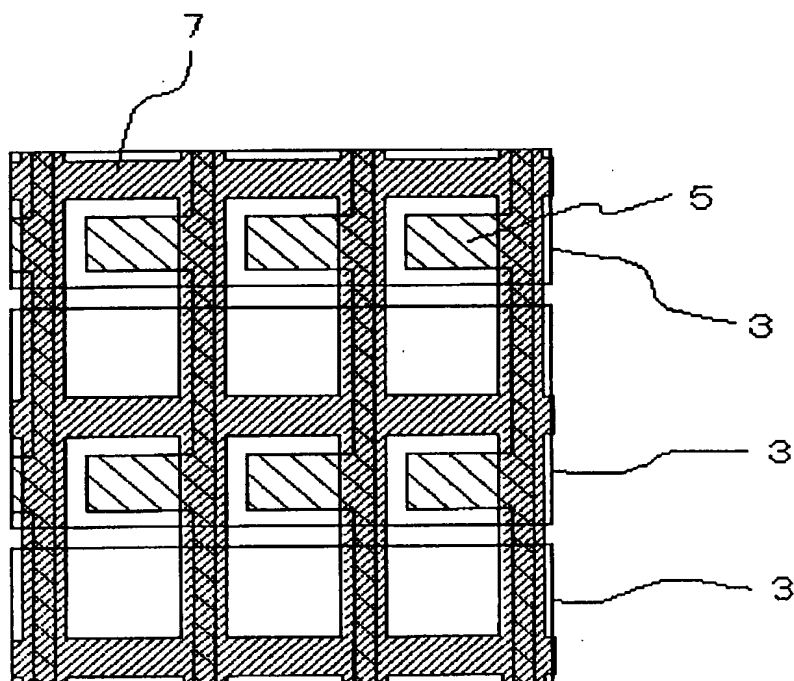
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FIG.13

P1 : PRIMING DISCHARGE PULSE
P2 : PRIMING DISCHARGE REMOVAL PULSE
P3 : SCAN PULSE
P4 : SCAN BASE PULSE
P5 : SUSTAIN PULSE
P6 : SUSTAIN REMOVAL PULSE
P7 : SCAN PULSE
P8 : SUSTAIN PULSE
P9 : DATA PULSE
P10 : DATA PULSE

FIG.14

P1 : PRIMING DISCHARGE PULSE
P2 : PRIMING DISCHARGE REMOVAL PULSE
P3 : SCAN PULSE
P4 : SCAN BASE PULSE
P5 : WRITE CANCEL PULSE
P6 : SUSTAIN PULSE
P7 : SUSTAIN REMOVAL PULSE
P8 : WRITE CANCEL PULSE
P9 : SCAN PULSE
P10 : SUSTAIN PULSE
P11 : DATA PULSE
P12 : DATE BASE PULSE
P13 : DATA PULSE

FIG.15A

FIG.15B

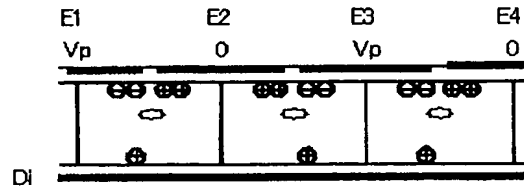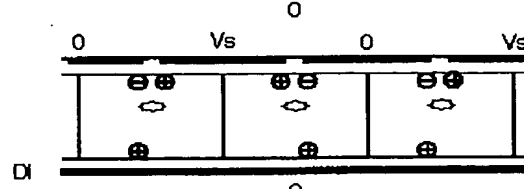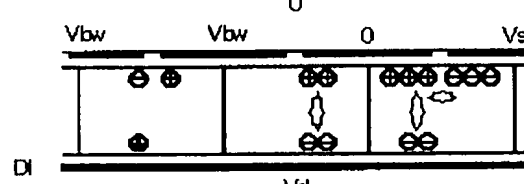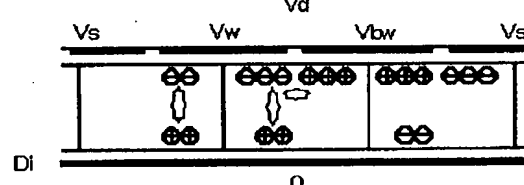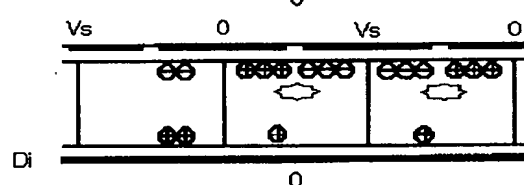FIG.15C

FIG.15D

FIG.15E

FIG.15F

FIG.15G

FIG.15H